

Figure 1  
(Prior Art)

MEMORY CONTROLLER

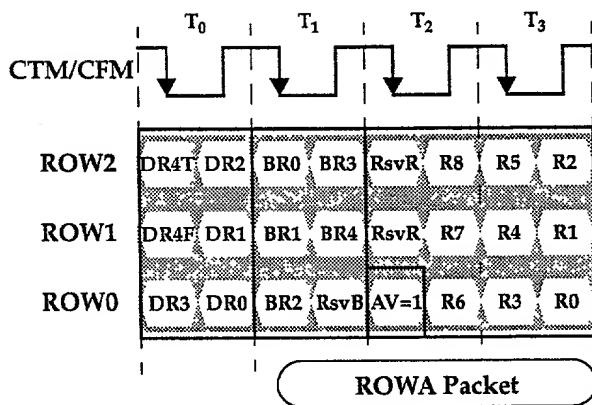


FIG. 2  
 (PRIOR ART)

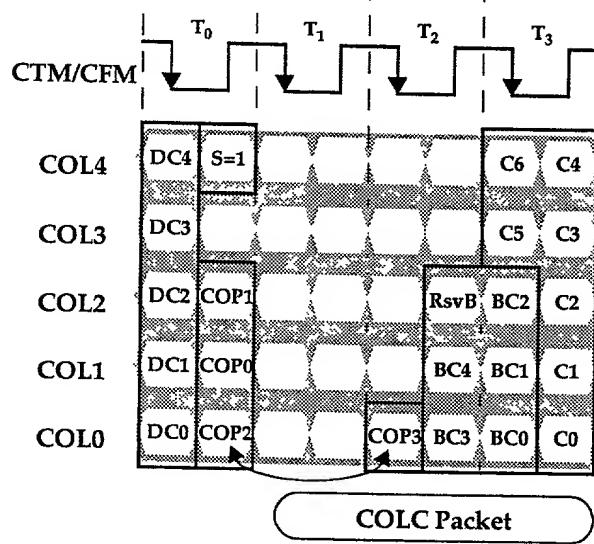


FIG. 3  
 (PRIOR ART)

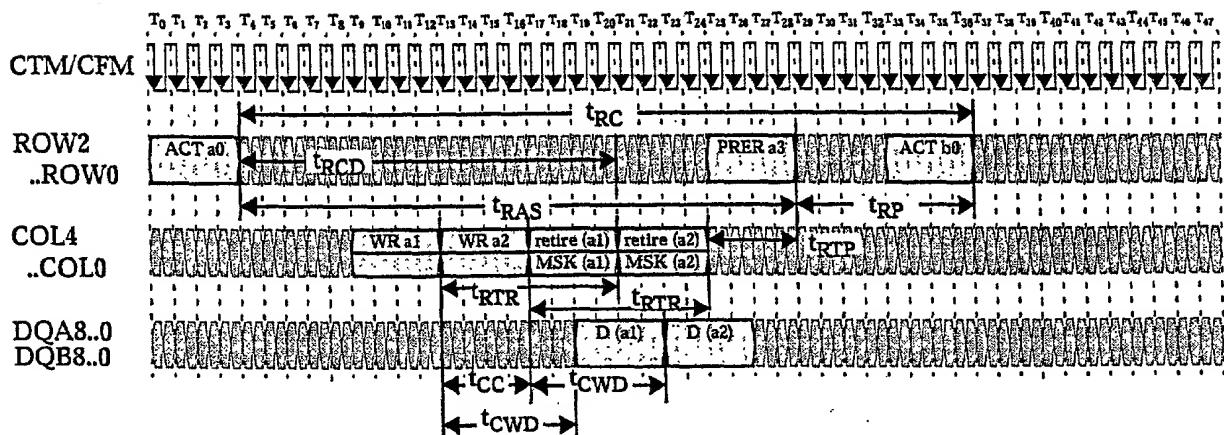


FIG. 4B  
(PRIOR ART)

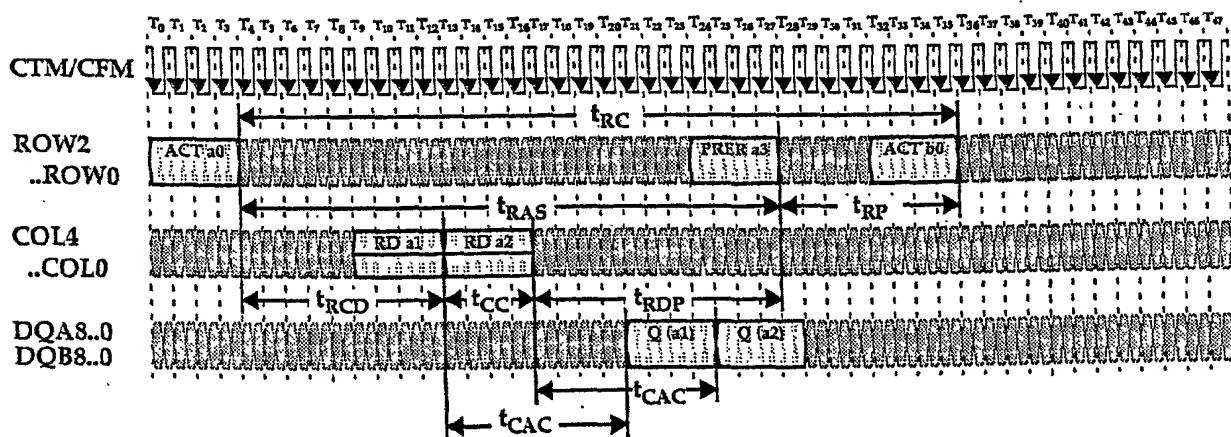


FIG. 4 A  
(PRIOR ART)

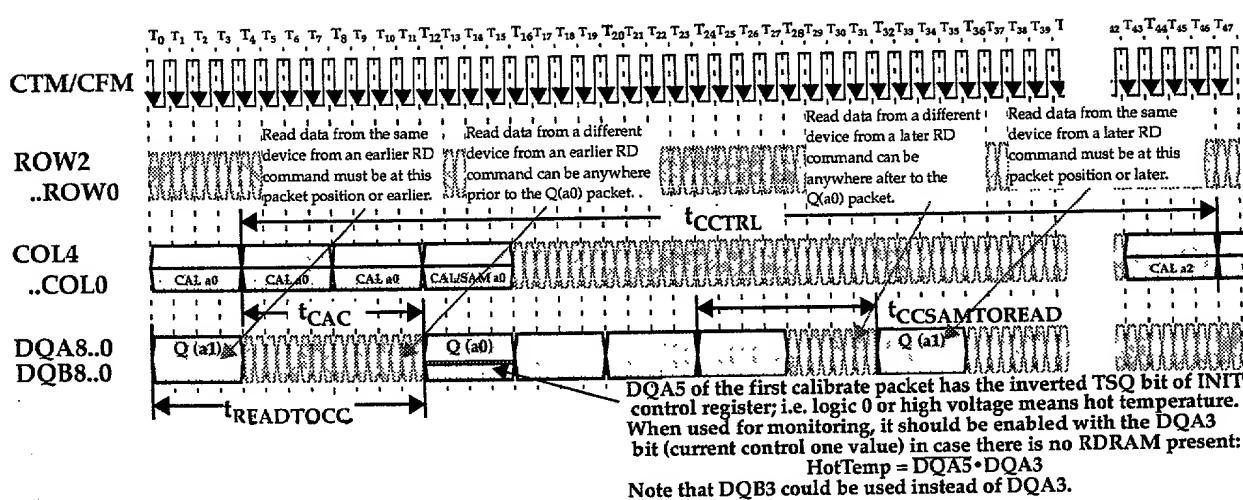


FIG. 5 (PRIoC ART)

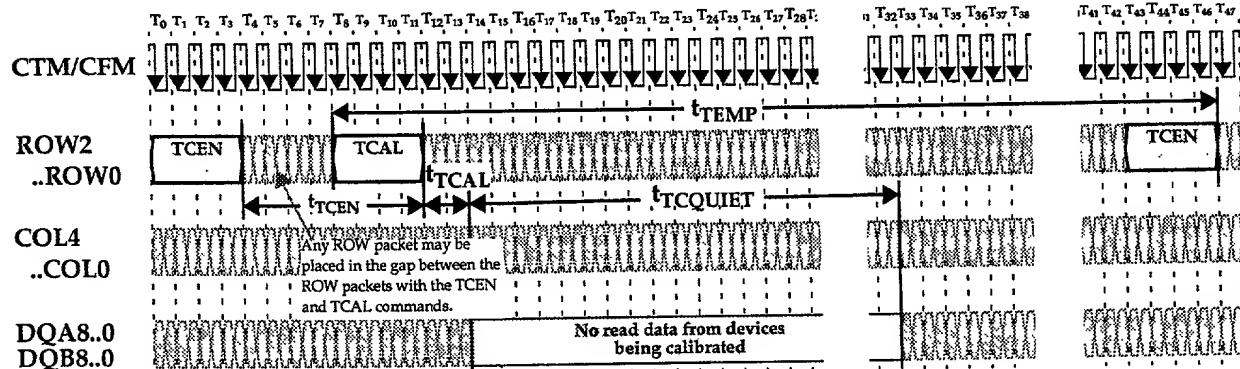


FIG. 6 (PRIoC ART)

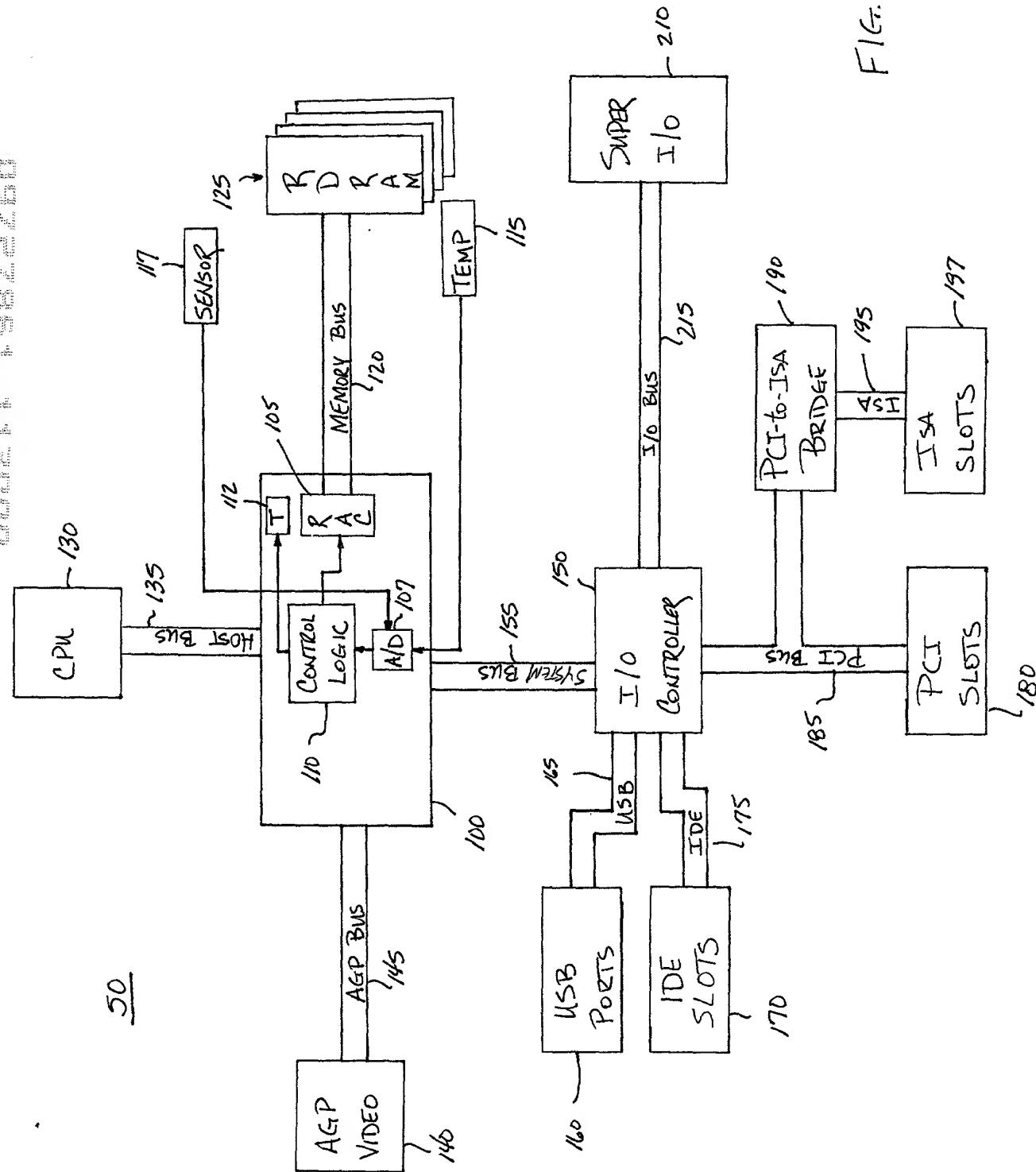
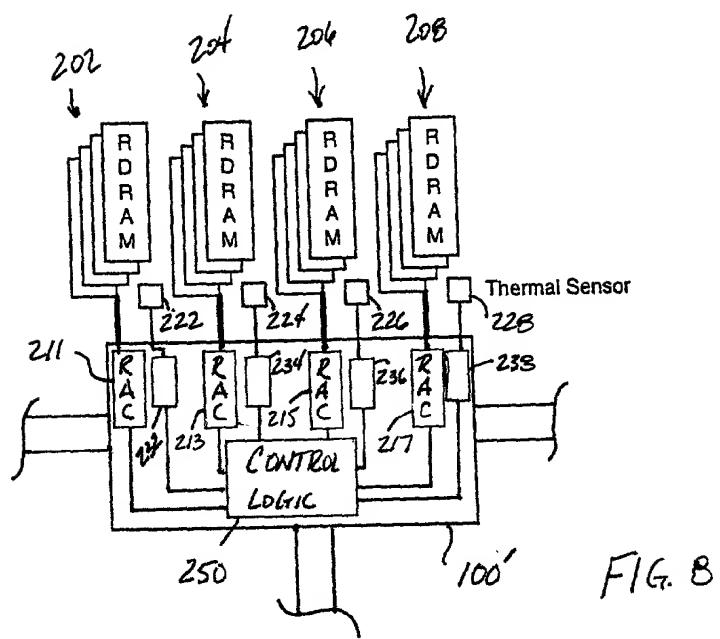


FIG. 7



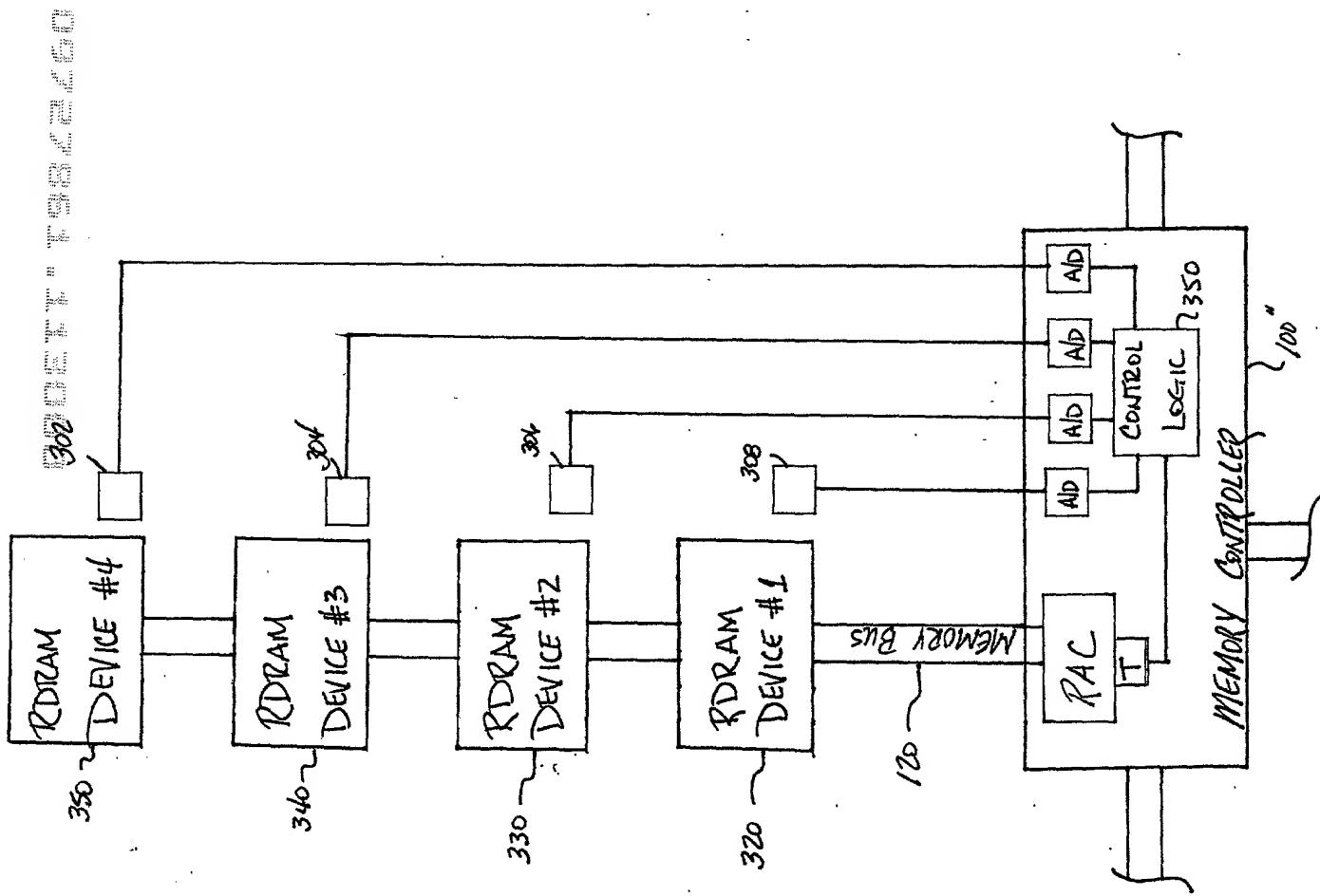


Figure 9

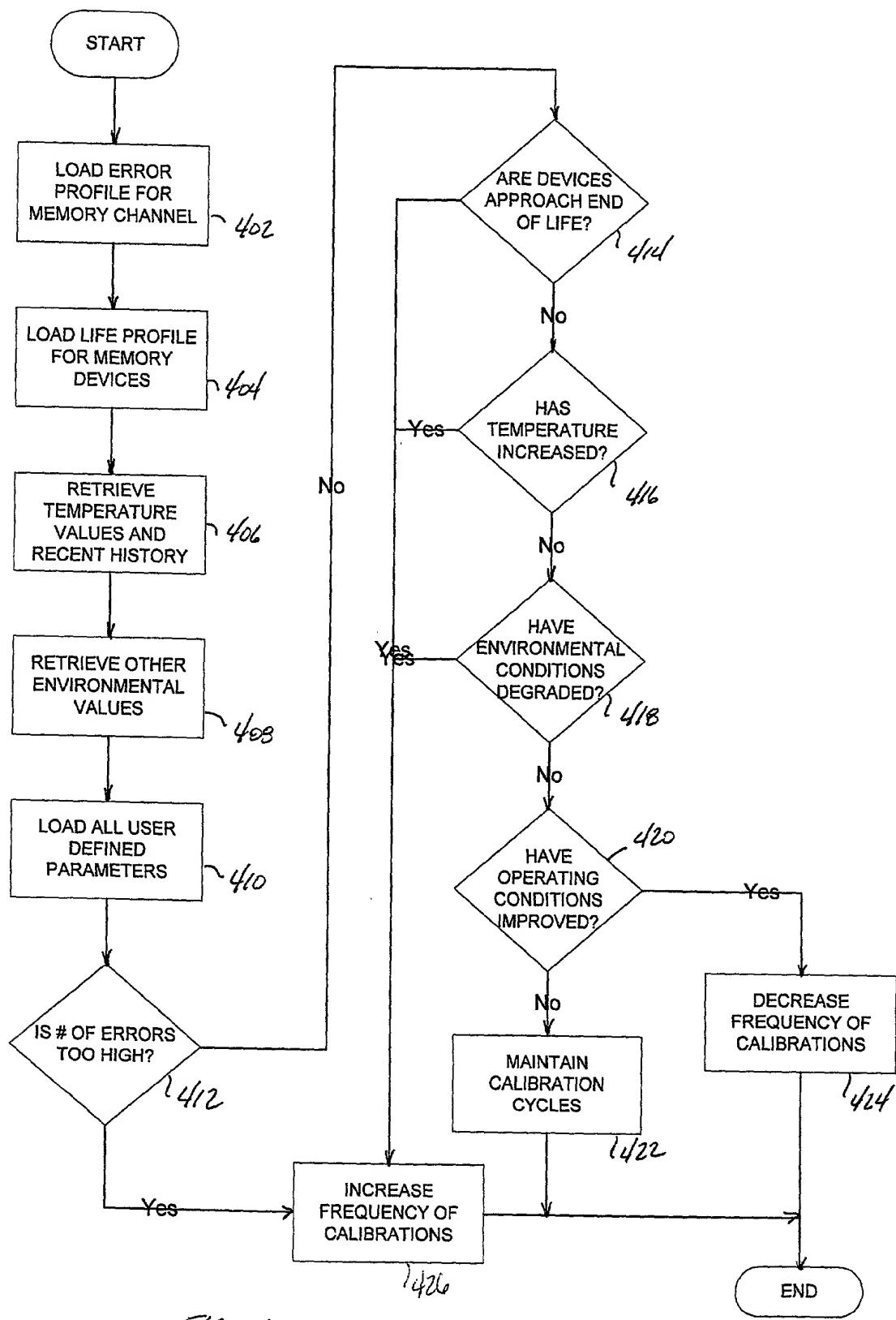


FIG. 10